RPS920010178US1

PATENT 3-14-0

-1-

UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Group Art Unit:

Victor R. Augsburg et al.

Not Yet Assigned

Serial No.:

Not Yet Assigned

IBM Corporation

Filed:

(Herewith)

Intellectual Property Law

3039 Cornwallis Rd.

:

Title: RE-ENCODING ILLEGAL OP CODES INTO A SINGLE ILLEGAL OP

CODE TO ACCOMMODATE THE EXTRA

BITS ASSOCIATED WITH PRE-DECODED

INSTRUCTIONS

Research Triangle Park, NC 27709

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents Washington, D.C. 20231

Dear Sir:

This Information Disclosure Statement is being submitted in connection with the above-identified application for patent. Applicants submit herewith patents, publications or other information of which they are aware, which they believe may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 C.F.R. § 1.56.

While this Information Disclosure Statement may be "material" pursuant to 37 C.F.R. § 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

1c971 U.S. PTO 10/082085 RPS920010178US1 PATENT

In accordance with 37 C.F.R. § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 C.F.R. § 1.56(a) exists.

The attached form, PTO-1449, provides a listing of patents, publications, or other information as required by 37 C.F.R. § 1.98(a)(1).

A copy of each of the items identified on the attached Form PTO-1449 is supplied herewith, except for the pending patent applications, for which no copies are being submitted.

Respectfully submitted,

WINSTEAD SECHREST & MINICK P.C.

Attorneys for Applicants

By:_

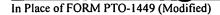
Robert A. Voigt, Jr.

Reg. No. 47,159

Kelly K. Kordzik Reg. No. 36,571

5400 Renaissance Tower 1201 Elm Street Dallas, Texas 75270-2199 (512) 370-2832

AUSTIN_1\183457\1 02/13/2002 - 7036-P198US



LIST OF PATENTS AND PUBLICATIONS FOR APPLICANTS' INFORMATION DISCLOSURE STATEMENT

Serial No.:

Applicants: Victor R. Augsburg et al.

Filing Date: (herewith)

Group:

Atty. Docket No.: RPS920010178US1

1c971 U.S. PTO 10/082085

Reference Designation

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
AAA	5,619,408	04/08/97	Black et al.	395	567	
ABC	5,717,587	02/10/98	Black et al.	364	131	
ACA	5,819,059	10/06/98	Tran	395	389	
ADA	5,940,602	08/17/99	Narayan et al.	395	389	
AEA	4,649,478	03/10/87	Worley, Jr.	364	200	
AFA	5,233,696	08/03/93	Suzuki	395	375	
AGA	5,337,415	08/09/94	DeLano et al.	395	375	
AHA	5,651,122	07/22/97	Hartung et al.	395	384	
AIA	5,852,741	12/22/98	Jacobs et al.	395	800.24	
AJA	5,970,235	10/19/99	Witt et al.	395	389	
AKA	6,263,429 B1	07/17/01	Siska	712	245	"
ALA						
AMA						
ANA						

FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
AOA						
APA						
AQA						

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner Initial	
ARA	J. A. Kahle, "Opcode Remap and Compression in Hard-Wired RISC Microprocessor," <i>IBM Technical Disclosure Bulletin</i> , Vol. 32, No. 10A, March 1990, p. 349.
ASA	V. R. Augsburg et al., Pending Patent Application docket No. RPS920010176US1 entitled "Efficiently Calculating a Branch Target Address"
Examiner:	Date Considered:

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

AUSTIN_1\183460\1 02/13/2002 - 7036-P198US